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Realization of Large Area Co₂₀Fe₆₀B₂₀-Based Perpendicular Magnetic **Tunnel Junction for CMOS Compatible Device Application**

Mohamad G. Moinuddin, Aijaz H. Lone, Srikant Srinivasan, and Satinder K. Sharma*®

School of Computing and Electrical Engineering, Indian Institute of Technology (IIT) Mandi, Mandi-175005 (Himachal Pradesh), India

Supporting Information

ABSTRACT: This study reports the current-induced switching of a Co₂₀Fe₆₀B₂₀/MgO/ $Co_{20}Fe_{60}B_{20}$ -based perpendicular magnetic tunnel junction (p-MTJ) with a $[Co/Pt]_4$ stack as a synthetic anti-ferromagnetic (SAF) layer. Electrical and spectroscopic evidence of p-MTJs reveals that the pinning of the Co₂₀Fe₆₀B₂₀ layer by the SAF through a Ta spacer layer enhances the switching resistance after annealing at 650 K (375 °C). A drastic improvement of 6 times in the tunneling magnetoresistance (TMR) was observed after annealing in an external magnetic field (14% for as-deposited and 83% for annealed both for zero bias). The TMR also shows a gradual increased behavior with bias at room temperature, which may be attributed to the multidomain switching with increase in current. The analogous SAF-based MTJ devices were modeled using the non-equilibrium Green's function (NEGF) to validate the experimental results. The existence of a stable TMR and the low switching current density of the fabricated p-MTJ structures provides higher reliability and low-power operation. This makes it attractive for next-generation MTJ applications, especially for embedded memory, system-on-chip, IoT, and neuromorphic computing applications.



KEYWORDS: perpendicular magnetic tunnel junction, increasing tunneling magnetoresistance, synthetic anti-ferromagnetic, low switching current density, non-equilibrium Green's function

1. INTRODUCTION

Emerging magnetoresistive random access memory (MRAM),¹ involving the integration of magnetic tunnel junction (MTJ) and CMOS technology, is a promising alternative for the existing nonvolatile semiconductor memory (NVM) applications, such as NAND Flash and others.²⁻⁴ In particular, MRAM shows much promise due to low power consumption, high endurance, and fast switching performance.⁵ The current challenges of MRAM lie in uniform resistance controllability, reliable switching of magnetic bits, and integration of MTJ with CMOS technology.

There is considerable interest to explore new CMOS compatible MRAM systems with novel functionalities like spin-transfer torque (STT), spin-orbit torque (SOT), and domain wall motion (DWM) for improved magnetic switching.8 Among all NVMs, STT-RAM has demonstrated the highest endurance (>10¹² cycles) and thermal compatibility for embedded back-end of line (BEOL) processed device applications, such as cache, big data memory, and high-end SoC.^{9,10} The foremost hindrances for realizing the real-world application of STT-RAM devices are the development of compatible magnetic materials, high-temperature stability, reliable switching, and device fabrication technology in line with an existing CMOS.^{11,12} For scalable and reliable STT-RAM, a low error probability obtained from the ratio of on and off currents is an important metric.¹³ In an MTJ, this ratio is reflected in the tunneling magnetoresistance (TMR) and usually decreases with bias. For stand-alone memory, TMR

needs to be greater than 200%. While subjected to hightemperature reaching 673 K (400 °C) or beyond for extended hours in the BEOL process, slightly lower TMR values of fabricated p-MTJs are considered to still be reliable for embedded memory application.¹⁴

In pursuit of attaining a high TMR, hitherto numerous ferromagnetic (FM)/oxide interfaces have been analyzed for symmetry filtering of tunneling current such as Fe/Al₂O₃, Co/ AlO_x, Fe/CrO₂, Fe/MgO, Co/MgO, and CoFeB/MgO.¹⁵ Among these, the half Heusler alloy ABX geometry, e.g., CoFeB has demonstrated high spin polarization (~65%) with perpendicular magnetic anisotropy (PMA).¹⁶

However, the stray field induced in the CoFeB/MgO-based MTJ system results in the asymmetric switching of the free layer by reducing the stability of the antiparallel state.¹⁴ This concern was addressed to a large extent by coupling of the reference FM layer with a synthetic anti-ferromagnetic (SAF) layer (consisting of FM/heavy metal (HM)/FM).¹⁷ This FM/ HM/FM stacking serves as a synthetic magnetic ordering isomorphic to a layered anti-ferromagnetic film. Carvello et al. reported the fcc (111) $L1_0$ FePt, $[Co/Pd]_n$, and $[Co/Pt]_n$ structures for SAF, which all result in perpendicular magnetic anisotropy.¹⁸ The presence of effective 3d-5p coupling between FM and HM layers in the FM/HM/FM structure

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Figure 1. Schematic process flow of p-MTJ fabrication and investigation of fabricated device structures; (a) deposition of bottom electrode pad of Pt/Ti. (b) Trench formation through 365 nm photolithography. (c) Deposition of MTJ stacks. (d) Annealing of MTJ in external perpendicular magnetic field at 650 K. (e) Schematic of complete STT-based p-MTJ with layers labeled as seed layer Ta, $C_{20}Fe_{60}B_{20}$ free layer, MgO tunneling layer, $C_{20}Fe_{60}B_{20}$ reference layer, Ta spacer, Co/[Co/Pt]₄ SAF layer, and Ta capping layer.

favors either parallel or antiparallel magnetization configuration of two corresponding FM layers depending upon the spacer thickness.^{19–21} Also, stacked SAF layers with the FM layer below a certain of a thickness (e.g., Co < 2 nm) have shown PMA with considerable anti-ferromagnetic behavior and thermal stability.²² The aforementioned developments have led to both high thermal stability and high TMR. In turn, this has paved the way for higher temperature device processing and reduced device dimensions allowing a CMOS–p-MTJ compatible memory cell (1T-1R) for a next-generation technology node.²³

The present work demonstrates field-free STT controlled switching of $Ta/[Co/Pt]_4/Co/Ta/Co_{20}Fe_{60}B_{20}/MgO/Co_{20}Fe_{60}B_{20}/Ta-based p-MTJ system with enhanced TMR at room temperature for nonvolatile memory application. The behavior of a single SAF <math>[Co/Pt]_n$ has been extensively studied and used for pinning and thermal stabilization of the $Co_{20}Fe_{60}B_{20}$ reference layer (RL) while reducing the device structure complexity and effective height.²⁴ Further, the experimental results are supported by the nonequilibrium Green's function (NEGF) and the theory of interlayer exchange coupling-based simulations.

2. MATERIALS AND METHODS

2.1. Materials and Fabrication. In order to investigate the current-induced switching (CIS) in CoFeB-based p-MTJ structures, a set of devices were fabricated as per the process flow as shown in Figure 1a–d. The standard MTJ blanket was as follows: $Pt(30)/Ta(5)/[Co/Pt]_4/Co/Ta/C_{20}Fe_{60}B_{20}(2)/MgO(1.8)/C_{20}Fe_{60}B_{20}(2)/Ta(05)/Pt(30)/Ti(10)/SiO_2(300)/Si (the number mentioned in parentheses indicate the thickness of layers in nm). The p-MTJ layers$

were deposited over a thermally oxidized silicon substrate using multitarget DC and RF plasma sputtering system at a base pressure of 5.0×10^{-7} mbar. As depicted in Figure 1e, the multilayer device structure consists of Ta (05) seed layer; $C_{20}Fe_{60}B_{20}$ (2) free layer (FL); MgO (1.8) tunneling layer; C₂₀Fe₆₀B₂₀(2) reference layer; Ta (1.2) spacer; Co/[Pt/Co/Pt/Co/Pt/Co] SAF pinning layer (presented as $Co/[Co/Pt]_4$ in this Article); and Ta (5) capping layer. Figure 1a-d shows the subsequent device processing steps: multistep standard photolithography, thin-film stack deposition, annealing and metallization of various processing steps. In this process, the bottom Pt/Ti (30/5) electrode was deposited over a standard RCA cleaned, thermally oxidized Si wafer, which was followed by typical standard photolithographic steps to pattern 50 \times 50 μ m² trenches over a positive photoresist. The whole MTJ blanket was deposited over the patterned substrate at 300 K and at a base pressure of $\sim 9.0 \times 10^{-7}$ mbar. Co₂₀Fe₆₀B₂₀ FM layers were deposited from a CoFeB (1:3:1) alloy target using 90 W, argon (Ar) plasma-based RF magnetron sputtering at 5×10^{-3} mbar. A MgO tunnel barrier was also sputtered from a 99.99% pure single crystal MgO target and sandwiched between the free and reference layer. Thereafter, the layers of [Co/ Pt]4 were deposited as Co (RF) and Pt (DC) co-sputtering from 99.999% pure Co and Pt targets over the rotating substrate. Afterward, a lift-off process was performed for realization of final p-MTJ structures from the deposited blanket. These p-MTJ stacks were annealed at 650 K (375 °C) under a 1 kOe perpendicular magnetic field at a vacuum of 10^{-6} mbar to achieve the PMA as shown in Figure 1d.

The study of the surface morphology and spectroscopic investigations are accomplished using field emission scanning electron microscopy (FE-SEM, Nova FEI), X-ray diffraction (XRD) spectroscopy (Bruker), X-ray photoelectron spectroscopy (XPS, Thermo-Scientific), vibrational sample magnetometery (VSM, Quantum Design), and atomic force microscopy (AFM, Bruker), whereas the



Figure 2. Fabricated p-MTJ devices. (a) XPS depth profile of annealed p-MTJ device stack with Pt bottom layer; all core spectra analyzed with 500 eV etch energy Ar⁺ plasma. (b) Free layer CoFeB material distribution. (c) Tunnel current investigation across the barrier; the I-V characteristics were fitted with a modified Simmons' expression for low bias; the spectra show a good fit for 1.06 eV (thick black); the inset shows the experimental tunnel current. (d) Barrier height calculated with ultraviolet photoelectron spectra for individual materials show the φ_B of 0.93 eV. (e) Capacitive behavior analyzed across the tunnel barrier; the main panel shows the frequency–capacitance response for a 25 mV AC signal, and inset shows a stable capacitance at 100 kHz.

fabricated p-MTJ structures were analyzed using the four-probe I-V measurement tool, Keithley 4200 SCS.

2.2. Modeling and Theoretical Analysis. To study the transport characteristics of the device, an effective mass tight-binding (EMTB) model was used to represent the Hamiltonian of the active region (CoFeB/MgO/CoFeB), and non-equilibrium Green's function (NEGF)-based spin transport simulations were conducted for the fabricated p-MTJ structures, following existing MTJ theory.²⁵ The different device characteristics studied include the current versus voltage (I-V) characteristics, resistance versus voltage (R-V)characteristics, and an estimation of different spin torques acting on the FL. Following the mode space analysis, a device model Hamiltonian consisting of a one-dimensional (1D) discrete chain of lattice points with lattice spacing $a_{fm} = 2.858$ Å (CoFeB lattice constant) and $a_{mgo} = 4.2$ Å was set up for each transverse mode described by wave vector k_t . These lattice parameters were extracted from crystallographic analyses of CoFeB and MgO. Other material parameters used in simulation are^{8,26} the Fermi level of CoFeB (2.25 eV), the exchange splitting in CoFeB (2.15 eV), the barrier height $(\varphi_{\rm B})$ between CoFeB and MgO (0.9–1.2 eV), the effective mass of the electron in CoFeB $(0.8m_{o})$, and the effective mass of electron in MgO $(0.33m_o)$; here, m_o corresponds to the free electron mass. These values are used with a small modification justified by some band structure nonidealities.²⁵ This single band Hamiltonian matrix effectively describes the device behavior in terms of hopping parameters t_{fm} and t_{mgo} , where $t = (\hbar^2/2ma^2)$ and \hbar = the reduced Planck's constant, m = effective mass, and a = lattice constant. The coupling of a semi-infinite electrode with the device is modeled in terms of self-energy,⁴ for the left and right contact self-energy matrices Σ_L and Σ_R were calculated and along with H used in the expression for the retarded Green's function (eq 1)

$$G = [(E + i0^{+})I - H - \Sigma_{L} - \Sigma_{R}]^{-1}$$
(1)

The broadening matrices of the left and right (eqs 2a and 2b) contacts are expressed as

$$\Gamma_L = i(\Sigma_L - {\Sigma_L}^+) \tag{2a}$$

$$\Gamma_R = i(\Sigma_R - {\Sigma_R}^+) \tag{2b}$$

The density spectral function *A*, from which we compute local density of states of the device, is given by

$$A_L(E) = G\Gamma_L G^+ \text{ and } A_R(E) = G\Gamma_R G^+$$
(3)

Similarly, local electron density is obtained from the diagonal element of the electron correlation function matrix given by

$$G'' = A_L f_L + A_R f_R \tag{4}$$

which is used in the transport equation (eq 5) along with other computed matrices to numerically compute the charge current and spin current versus voltage characteristics of the MTJ. The different current components are calculated with the expression.

$$I_{(y)} = trace(YI_{op}) \tag{5}$$

Here, Y represents the identity matrix for charge current, and the Pauli spin matrix σ represents spin current.^{5,4} The current density operator is expressed as

$$I_{op} = \frac{i}{h} \left\{ (HG^{n} - G^{n}H^{+}) + \left(\sum G^{n} - G^{n} \sum^{+} \right) + \left(\sum^{in} G^{+} - G \sum^{in} \right) \right\}$$
(6a)

$$I = \frac{i}{h} \sum_{k_t} \left\{ (HG^n - G^n H^+) + (\Sigma G^n - G^n \Sigma^+) + \left(\sum^{in} G^+ - G \sum^{in} \right) \right\}$$
(6b)

Finally considering coherent transport and conservation of the transverse momentum vector (k_{\parallel}) , the 3D structure, which has been effectively modeled as 1D paralleled and uncoupled transverse modes, allows us to compute current for each transverse mode individually from eq 6b over the energy range $(E_f \pm 10k_BT)$, where k_B is the Boltzmann constant, and T is the temperature (assumed 300 K). Finally, the currents at different transverse energies were integrated with respect to transverse energy. It is to be noted that small adjustments were done in m_{ox} and $U_b(\varphi_B)$ within the range prescribed in ref 25 to get a close agreement with experimental data.

3. RESULTS AND DISCUSSION

The morphology analyses of fabricated p-MTJ devices were performed to confirm the uniform surface topography and also to compute the device dimensions. FE-SEM micrographs (topdown) for p-MTJ device structures of \sim 50 \times 50 μ m² along with a device stack thickness (avg $t_{mtj} \approx 22$ nm) profile are shown in Figure S1a,b. The Co₂₀Fe₆₀B₂₀ free FL's surface roughness as measured by AFM is shown in Figure S1c and found to be ~ 0.5 nm (r.m.s), which confirms the smooth oxide/FL interface. To reveal the high-quality interface and existence of [Co/Pt]₄SAF after annealing of the thin-film stack, XPS depth profiling analyses for [Co/Pt]₄/Co/Ta/ $C_{20}Fe_{60}B_{20}(2)/MgO(1.8)/C_{20}Fe_{60}B_{20}(2)/Ta(05)/Pt(30)$ were performed. Figure 2a shows the traces for each material of the MTJ blanket with relative concentration at the given depth.²⁷ The interface position indicated in Figure 2a clearly depicts the layer distribution in the MTJ structure.^{22-24,28} To establish perpendicular magnetic anisotropy (PMA), the material composition distribution in iron-enriched CoFeB was analyzed by XPS over a control sample (CoFeB/Ta/Si). The atomic distribution of Fe:Co:B was found 51.46:26.5:21.89 %, as shown in Figure 2b. The tunneling charge transport analysis from experimental data clearly suggests a nonlinear transmission across the p-MTJ barrier MgO (1.8 nm). Figure 2c (inset) depicts the experimental data (thick black line) for tunneling across MgO. Here, the barrier height of MgO was determined using a modified Simmons' expression for low bias as shown in Figure 2c.^{29,30} The fitted model indicates a tunnel barrier $\varphi_B \approx 1.068$ eV (details of the fitting parameter explained in Figure S3), which was further analyzed through valence band spectral analysis. The work function (φ) of the Co₂₀Fe₆₀B₂₀/MgO/Co₂₀Fe₆₀B₂₀ structure was extracted from the ultraviolet photoelectron spectroscopy (UPS) measurements using the He⁺ source of 21.2 eV for MgO and CoFeB, and they were found to be 4.688 and 5.616 eV, respectively. The barrier height computed for $Co_{20}Fe_{60}B_{20}/$ MgO is 0.928 eV as inferred from Figure 2d, which is in close agreement with the $\varphi_{\rm B}$ obtained from fitting the electrical characteristics.³¹

The major issue of a large area MTJ is film uniformity and pinhole formation due to metal diffusion or tunnel barrier discontinuity, which leads to the anomalous variation in the TMR as reported by Useinov et al.³² The electrical reliability of MgO as a tunnel barrier has been established by considering the fabricated MTJ stack as analogous to an MIM system. Charge accumulation at the FM/MgO interface provides significant capacitance. Figure 2e shows the capacitance of tunnel barrier across a substantial range of AC signal frequency (25 mV × cos(*wt*)) superimposed over the DC signal. It was found that this capacitance reduces with strong dependence at low frequency (f_{low} < 10 kHz) and weak dependence at high frequency (f_{high} > 50 kHz). At high frequency (100 kHz), the p-MTJ has shown a stable capacitance of 291 ± 1 pF (11.64 ±



Figure 3. Current controlled switching characteristics for fabricated p-MTJ structures. (a) I-V characteristic of p-MTJ with different resistive paths (AP: antiparallel and P: parallel); bias switching shown as $V_{P \rightarrow AP} = -0.85$ V and $V_{AP \rightarrow P} = 0.65$ V, respectively, in $\log(I)$ vs V characteristics (inset). (b) Resistance vs voltage plot for P and AP configurations; an enhanced TMR of 83% was observed at zero bias.

0.1 μ F/cm²) within a device operation bias of $-1 \rightarrow +1$ V, which depicts the linear q-V relation without bias dependent charge dissipation inside the tunnel barrier. Moreover, there are a few reports regarding the magnetocapacitance at low-frequency AC signal analysis for MgO-based MTJs, which has to be systematically analyzed in a future publication.^{33,34}

3.1. Current-Induced Switching Analysis. Figure 3a shows the measured I-V characteristics of $Ta/[Co/Pt]_4/Co/$ $Ta/Co_{20}Fe_{60}B_{20}/MgO/Co_{20}Fe_{60}B_{20}/Ta$ structures in the absence of an external magnetic field, demonstrating currentinduced switching (CIS) of the FL due to spin torque exerted by the RL.³² As depicted in Figure 3a, the Co₂₀Fe₆₀B₂₀-based p-MTJ device follows the antiparallel (AP) and parallel (P) current paths when sweeping from -1 to $1 \vee (1 \rightarrow 2 \rightarrow 3)$ and from 1 to $-1 \text{ V} (4 \rightarrow 5 \rightarrow 6)$, respectively. It reveals the different resistance states of the p-MTJ device at the forward and reversed current directions. These resistance paths correspond to the parallel (P) and antiparallel (AP) magnetization states of the FL with respect to the RL.³³ The applied biases to change the resistance states of p-MTJ structures from the parallel to the antiparallel state and vice versa were found to be $V_{P \to AP} = -0.85$ V and $V_{AP \to P} = 0.65$ V, respectively (as shown in Figure 3a (inset)). This asymmetric response of switching voltage is explained by the voltage asymmetry of the spin-transfer torque on the FL.^{25,35,36} It was also observed that after annealing, the tunneling current is enhanced in the device. The amorphous tunnel barrier has higher electron scattering inside the barrier, resulting in high barrier resistance and low TMR.37,38

The calculated $\text{TMR} = \frac{R_{AP} - R_P}{R_P}$ from Figure 3b for asdeposited p-MTJ was found to be 14.1%, whereas for the annealed p-MTJ, the TMR increased to 83%. The annealing enhances the crystallinity of the ferromagnets and MgO and the quality of the interface between FM and MgO. The improved quality of the oxide and interface improves the band filtering property of MgO and the interface, thus enhancing the TMR up to 83%. Moreover, the improved crystallinity of the FM reduces the number of domains, leading to increased stability. This, in turn, increases the switching current for the annealed device to 6 mA, as compared to 2 mA in the asdeposited case. To understand the tunneling mechanism in the amorphous and annealed system, we have analyzed the blanket p-MTJ with different spectroscopic investigations (discussed later in Section 3.2).^{39,40} As shown in Figure 3b, a TMR of 83% was achieved at zero bias and was found to increase slightly with increasing bias. The resistance states of p-MTJ structures were found to be stable within the read voltage bias of ± 0.2 V.¹⁵ Thus, the existence of a stable TMR and low switching current density in the fabricated p-MTJ structures provides higher reliability with regard to long cycle data retention and low-power operation. This makes it attractive for next-generation MTJ applications, especially for embedded memory and system-on-chip applications, where it has to sustain high-temperature CMOS (BEOL) processing.

Based on the analysis described above, a switching schematic is proposed that consists of four different configurations of the multidomain FL under distinct bias conditions as shown in Figure S5.⁴¹ Due to the multi-domain existence in FL, at zero bias, some domains are not properly aligned toward both P and AP states, which results in the reduced effective magnetization of FL. The zero-bias resistance corresponds to this average effective magnetization orientation between FL and RL. While increasing bias in the AP state from $0 \rightarrow 0.65$ V, we observe almost a bias independent resistance state and the multi-level switching until complete magnetization reversal to the P state as shown in Figure 3b. In the reverse direction starting from the P state, $0.65 \rightarrow -0.8$ V, we observe a nonlinear path due to the alignment of residual domains, and finally, switching from P to AP is abrupt and occurs at a large bias of -0.8 V. The different resistance trends with varying bias for the P and AP paths may be attributed to the dominance of the ferromagnetic type interlayer exchange coupling between RL and FL, which makes the parallel state stable. Also, the voltage asymmetry of STT due to bias dependence of spin polarization results in this asymmetrical switching.⁴¹

Since the real chips may operate at elevated temperatures, a high-temperature aging is usually performed to match standard CMOS process conditions. The fabricated MTJ has been analyzed for the temperature range from 296 to 380 K. A temperature ramp rate of 0.25 K/s was applied along with the retention cycle to witness the p-MTJ thermal stability. Figure 4 shows real-time resistance (R_p) analysis for a read bias of 0.2 V.

Initially, the MTJ was found in the parallel state with the R_P of ~160 Ω and found stable up to ~353 K (~80 °C). The effect of temperature variation on the magnetization stability of FL and RL is already well-known. Further, the increasing temperatures also alter the coupling between SAF and RL, which in turn results in the FL switching (as perceived from Figure S6).⁴² Nevertheless, the stable resistance state up to



Figure 4. Resistance state stability of fabricated p-MTJ under increasing temperature.

 \sim 353 K for the fabricated p-MTJ was found suitable for CMOS embedded applications.

The device was modeled using known theory to realize the TMR behavior of the fabricated MTJ. Figure 5a shows the active region for the modeled MTJ. Figure 5b shows the parabolic approximation of energy bands including the various transverse modes, based on the effective mass tight-binding model for the active region of the MTJ (CoFeB/MgO/CoFeB). The I-V and R-V characteristics were computed by

Article

the effective mass band tight-binding (EMTB) non-equilibrium Green's function-based spin transport model. The assumption of a coherent interface between MgO and CoFeB allows us to use the mode space approach to break the 3D bulk transport problem into multiple 1D problems, each with a different transverse energy. The parameters used in modeling include the lattice constants of FM (2.89 Å) and MgO (4.05 Å) taken from crystallographic analysis and the effective mass of the electron in CoFeB (0.8 m_o) and MgO (0.2 to 0.33 m_o) varied to fit experimental results considering impurity diffusion in the barrier and multi-domain FM system, along with the obtained barrier height of MgO (φ_B or $U_b = 0.9$ to 1.2 eV). The exchange splitting in CoFeB was chosen to be 2.15 eV, while the Fermi energy level of CoFeB was set to 2.25 eV, as adopted from Datta et al.³⁴

Figure 5c depicts the bias dependent conduction characteristics in the parallel state configuration. The differential tunneling conductance was calculated by g(V) = dI/dV for the as-deposited and annealed samples. It was observed that the annealed MTJ provides an enhanced conductance of 4.5 mS as compared to the as-deposited sample (1.85 mS). The tunneling current and TMR characteristics for both experiment and simulation are observed to be following each other in the read bias range (-0.28 to 0.4 V) with positive slope conductance. However, in the antiparallel state, the conductance for annealed MTJ does not alter much with bias (0.01 < V_{bias} < 0.4 V), producing a gradual increase in TMR. This constant conductance for the annealed MTJ has been



Figure 5. Tunneling analysis through the oxide barrier. (a) Schematic of a compact model for the active device regime; (b) energy band diagram for the analogous modeled MTJ. The left contact is a free FM layer, and an insulator (MgO) oxide introduces a tunneling barrier of height of 0.90– 1.2 eV. The switching of free FM shows changes in conduction from parallel to antiparallel states. Experimental and numerical simulation of bias dependent characterization of p-MTJ for (c) parallel and (d) antiparallel configurations of MTJ; modeled electrical characteristics show a good match at low bias. Here, conductance g(v) is shown in blue.

Table 1. Summary and Comparison of $[Co/Pt]_n$ or $[Co/Pd]_n$ -Based MTJs

MTJ stack structure (thickness in Å)	pinned layer	annealing (summary in K)	switching phenomena	TMR (%)	reference
$ \begin{array}{l} Ta(50) / [Co(6) / Pd(18)]_x Co(07) / Mg(05) / MgO(21) Co(07) / \\ [Co(7) / Pd(18)]_2 \end{array} $	-	423	FIS	11	Kugler et al. ⁴⁵
$\begin{array}{l} Ta(30)/Pt(100)/[Co(6)/Pt(18)]_4/Co(7)/AlO_x/[Co(7)/Pt(18)]_2/\\ Pt(32) \end{array}$	-	673	SMTE	15	Park et al. ⁴⁶
$\begin{array}{l} CoFeB(15)/MgO(10)/CoFeB(11)/Ta(04)/Co(05)/[Co/Pt](40)/\\ Ru(8.5)/Co(05)/[Co/Pt](65) \end{array}$	Ru, Hf, and Hf/Ru	673	SMTE	100	Chatterjee et al. ⁴⁷
CoFeB(10)/MgO(10)/CoFeB(15)/[Co/Pt](50)/Ru(8.5)/ [Co/Pt] (100)/Ru(50)	[Co/Pt]/Ru/[Co/Pt]/Ru	598	STT-SOT	35	Kan et al. ⁴⁸
$Ta(50)/[Co/Pt]_4/Co(80)/Ta(12)/CoFeB(20)/MgO(18)/$ CoFeB(20)/Ta(50)	$Co/[Co/Pt]_4$	650	STT	83	this work

"The abbreviations are FIS: field-induced switching; SMTE: spin-momentum transfer effect; and STT: spin-transfer torque effect.



Figure 6. (a) X-ray diffraction (XRD) spectra of fabricated MTJ (as-deposited and annealed). Comparison of the bare substrate, as-deposited, and annealed samples showed crystallization after annealing. (b) XRD of free layer $Co_{20}Fe_{60}B_{20}(2)/Ta(5)/Si$ as a control sample. (c) Fitted curve with possible peaks for annealed MTJ; convoluted peaks showed the appearance of Pt (111) contact electrodes and a Co/Pt (111) SAF layer.

explained due to the multi-domain pinning in the antiparallel state after crystallization.⁴¹ Considering the large area free layer, the fitted model also showed constant conduction for the

bias region as shown in Figure 5d. The modeled results are in close agreement in the parallel state throughout the bias range; while in the antiparallel state, there is a good match in the low



Figure 7. VSM hysteresis loops measured (a) in-plane and out-of-plane field hysteresis loops of the thin free layer with ~2 nm after annealing at 650 K (375 °C). The M-H loop shows the out-of-plane easy axis getting saturated at 2.5 kOe (as shown in schematics) (b) for the Co/Pt multilayer. (c) Out-of-plane M-H hysteresis loop for p-MTJ blanket with $Pt/[Co/Pt]_4/Co/Ta/Co_{20}Fe_{60}B_{20}/MgO/Co_{20}Fe_{60}B_{20}/Ta/Pt/SiO_2/Si stacks. The low-temperature (100 K) <math>M-H$ depicts the dominant anti-ferromagnetic behavior of Co/Pt below TB (shown in inset). (d) Modeled [Co/Pt/Co] SAF showing oscillation in AFM–FM ordering with variation in spacer (Pt) thickness and an analogous schematic sketch of spin coupling (inset).

bias $(\pm 0.4 \text{ V})$.⁴³ We make note that small modifications were done with the effective mass of MgO, which can be justified due to the small impurities in the ultrathin oxide tunneling layer, as it can alter the band structure of MgO, so the effective

mass
$$m_o^* \propto \left(\frac{1}{h^2} \frac{d^2 E}{dk^2}\right)^{-1}$$
. 22,25,44

The electrical performance of the investigated Ta/[Co/Pt]₄/Co/Ta/Co₂₀Fe₆₀B₂₀/MgO/Co₂₀Fe₆₀B₂₀/Ta p-MTJ structures showed promising STT-based device operation with enhanced TMR while reducing the stack size and materials used. Table 1 presents a summarized comparison of the present work with the previously reported $[Co/Pt]_n$ -based MTJ structures.

3.2. Crystal Structure Analysis: Annealing Effect on p-MTJ Structures. XRD is used to confirm the crystal orientation of high-quality, nanoscale-thin $Ta/[Co/Pt]_4/Co/Ta/CoFeB/MgO/CoFeB/Ta/Pt/Ti/SiO_2$ p-MTJ multilayer structures. Figure 6a shows the XRD spectra of the asdeposited and annealed p-MTJ at 650 K (375 °C) for 1 h. For the as-deposited films, the absence of specific desirable signature peaks in the MTJ blanket spectrum indicates the amorphous behavior.⁴⁹ On the other hand, the XRD spectra for the annealed MTJ blanket clearly reveal the crystallization of $Co_{20}Fe_{60}B_{20}$ and Ta buffer layer in the (200) crystal orientation on either side of the tunnel barrier layer. Figure 6b provides a clear understanding of the post annealing crystallization of the $Co_{20}Fe_{60}B_{20}$ FL with a Ta seed layer, i.e., only a two-layer structure as a control experiment versus the test MTJ stack structures. The orientation of (200) $Co_{20}Fe_{60}B_{20}$ crystal planes were observed under the influence of the Ta seed layer (200) crystal planes.^{50,51}

It is also be seen that Pt bottom and top electrodes' thin films exhibit the most stable fcc (111) orientation, along with other trivial (200) peaks. There is a significant peak shift in Pt (111) observed in the postannealed MTJ blanket. As Figure 6a consists of a bottom Pt layer (30 nm) and top $[Co/Pt]_4$ layers (9 nm), this shift in peak position can be attributed to the enhancement of the $[Co/Pt]_4$ (111) crystal orientation. Figure

6c represents the detailed peak position of the fcc Pt (111) and fcc Co/Pt (111) oriented multilayer system. The superimposed spectra of diffraction peaks of Co (111) and Pt (111) in fcc $[Co/Pt]_4$ expresses the crystalline nature of the multilayer system. The XRD spectra of the annealed Co/Pt multilayer stack does not present any peak trace at 20°, which suggests the absence of the L1₁ CoPt alloy.⁵² Post annealing, the $[Co/Pt]_4$ multilayers get anti-ferromagnetically coupled and act as an SAF as shown in Figure 7b. The contributions of the fcc Pt (111) bottom layer and fcc Co/Pt (111) SAF have been studied, and their separate contributions are confirmed in p-MTJ (as shown in Figure S7).⁵³

3.3. Magnetic Response Analysis: Origin of Free Layer, SAF, and Pinning the Reference Layer. To verify the existence of perpendicular magnetic anisotropy (PMA) with well-defined magnetization switching behavior in Co₂₀Fe₆₀B₂₀, the FL was probed under an external field. Figure 7a shows the dependence of magnetization (M_{sat}) of FL with the external field in-plane and out-of-plane of the film surface. The saturation in perpendicular magnetization around the ~ 3 kOe indicates an out-of-plane easy axis of the Co₂₀Fe₆₀B₂₀ FL. On the other hand, the measured in-plane M-H orientation of $Co_{20}Fe_{60}B_{20}$ showed unsaturated behavior indicating the hard axis. The out-of-plane anisotropy of the FL starts dominating with decreasing film thickness and results in an out-of-plane easy axis orientation.54,55 The schematic in Figure 7a illustrates the magnetization saturation under different values of the external magnetic field. The FM dipoles with the perpendicular easy axis require significantly less energy to align with the out-of-plane magnetic field as compared to the in-plane magnetic field.

The magnetic response for the Co/Pt multilayer stack was analyzed for confirmation of anti-ferromagnetic ordering. The response showed a hysteresis loop that indicates the combined response of the multilayer stack instead of a layer by layer switching response. Figure 7b shows the M-H response of the Co/Pt multilayer stack and the effect of annealing on the magnetic ordering. The as-deposited sample showed the absence of anti-ferromagnetic behavior while annealing up to 650 K (375 °C) allowed crystallizing toward (111) orientation and effective exchange coupling between two consecutive Co layers. As shown in the loop $(1 \rightarrow 2 \rightarrow 3 \rightarrow 4)$ (left inset), the hysteresis region $(1 \rightarrow 2)$ is following a different slope than for the increasing magnetization path $(3 \rightarrow 4)$. The non saturating portion of the curve $(3 \rightarrow 4)$ in increasing field $(0 \rightarrow \pm 200)$ Oe) indicates a reverse remnant magnetization that may be attributed to the existence of dissimilar bubble domains.¹⁹ These bubble domains eventually align at the high magnetic field. At decreasing applied field ($\pm 200 \rightarrow 0$ Oe), the hysteresis region shows sharp decay, which indicates the reversal of the same bubble domains that were aligned at high field. These domains are anti-ferromagnetically ordered up to 200 Oe. The Arrot plot (Figure 7b (right inset)) provides the evidence of non saturating AFM in an SAF layer for a low field. $^{56-59}$ These $[\operatorname{Co/Pt}]_4$ and SAF get coupled with $Co_{20}Fe_{60}B_{20}$, resulting in the pinning of the RL.

Figure 7c shows the hysteresis loops for the fabricated p-MTJ blanket. In contrast with FL magnetization (Figure 7a), the M-H loop of the fabricated p-MTJ structures depicts the FM–SAF ferromagnetic coupling justified by the increased coercivity of ~200 Oe (higher than FL \approx 50 Oe).⁶⁰

The observed M-H curve describes the FL switching followed by RL switching in the high-field regime due to RL/

SAF coupling. The nonsaturating behavior of the M-H loop shows the anti-ferromagnetic coupling between Co/Pt/Co layers, which dominate at temperatures (figure shows 100 K) below the blocking temperature as shown in Figure 7c (inset).

The anti-ferromagnetic exchange coupling in crystalline $[Co/Pt]_4$ multilayers, a trilayer system, was modeled by using two-dimensional quantum well theory⁶¹ as shown in Figure 7d. The theoretical work function difference of 0.93 eV between Co (5.0 eV) and Pt (5.93 eV) results in a potential well between two ferromagnetic layers (Co/Pt/Co). It induces an oscillatory behavior in spin density with respect to the Pt thickness, resulting in a periodic oscillatory behavior of interlayer exchange energy (*J*) as shown in Figure 7d. It should be noted that *J* damps as $1/D^2$ where *D* is the thickness of the Pt layer. The simulated outcomes show antiferromagnetic behavior at 1.2 nm thick Pt layers, in agreement with the Pt (1.2 nm) thickness in the fabricated p-MTJ device structures. Thus, simulation and experimental results have investigated the use of single stack $[Co/Pt]_4$ for pinning RL.

4. CONCLUSION

In summary, we investigated the current-induced switching of a Co₂₀Fe₆₀B₂₀/MgO/Co₂₀Fe₆₀B₂₀-based p-MTJ with a [Co/ Pt]₄ stack as the SAF. Electrical and spectroscopic evidence reveal the pinning of the $Co_{20}Fe_{60}B_{20}$ layer by SAF through a Ta spacer layer, which enhances the switching resistance after annealing at 650 K (375 °C). A 600% increase in TMR was observed after annealing in an external magnetic field (14% for as-deposited and 83% for annealed both for zero bias). The TMR also shows the novel behavior of a gradual increase with bias at room temperature, which may be attributed to the multi-domain switching with increase in current. The analogous SAF based MTJ devices were modeled using NEGF to validate the experimental results. The existence of a stable TMR and the low switching current density of the fabricated p-MTJ structures provides higher reliability and lowpower operation. This makes it attractive for next-generation MTJ applications, especially for embedded memory, systemon-chip, IoT, and neuromorphic computing applications.

ASSOCIATED CONTENT

S Supporting Information

The Supporting Information is available free of charge on the ACS Publications website at DOI: 10.1021/acsaelm.9b00469.

Morphological analysis of the fabricated devices along with the surface roughness of the free layer (Figure S1); boron (B) distribution inside $Co_{20}Fe_{60}B_{20}$ FL and MTJ blanket confirmed using elemental analysis (Figure S2); calculation of barrier height characteristics of the tunnel barrier devices (Figures S3 and S4); proposed switching mechanism based on device experimental data (Figures S5 and S6); crystallographic evidence of $[Co/Pt]_n$ SAF (PDF)

AUTHOR INFORMATION

Corresponding Author

*E-mail: satinder@iitmandi.ac.in.

ORCID 0

Satinder K. Sharma: 0000-0001-9313-5550

Notes

The authors declare no competing financial interest.

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2278